



Fig.2

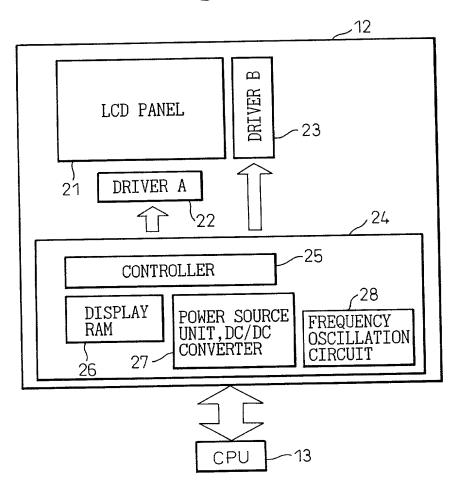


Fig.3

DEVICE AS TOTAL	LCD	RADIO	CONTROL
	DISPLAY	CIRCUIT	CIRCUIT
Δــــ1360	360µA	600µA	400µA



Fig.4

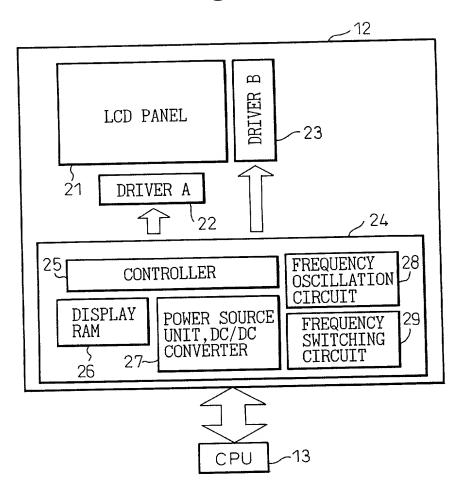


Fig.5 24 25 CONTROLLER 29 <sub>28</sub> 26 FREQUENCY SWITCHING CIRCUIT DISPLAY RAM FREQUENCY OSCILLATION CIRCUIT FREQUENCY SELECTION INTERFACE CIRCUIT -31 POWER SOURCE UNIT, DC/DC OSCILLATION FREQUENCY SELECTION CIRCUIT CONVERTER 32 BUFFER 30 27 FREQUENCY-DETERMINING EXTERNAL CIRCUIT FREQUENCY-DETERMINING EXTERNAL CIRCUIT  $\dot{3}_n$ 331



Fig.6

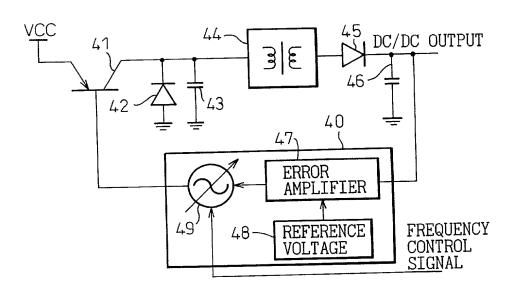


Fig.7A

SAMPLE HOLDING DC/DC OUTPUT (MIN)

Fig.7B

SAMPLE HOLDING DC/DC OUTPUT (MIN)

